

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q78657

Hien Boon TAN, et al.

Appln. No.: 10/581,395

Group Art Unit: Not Yet Assigned

Confirmation No.: 3868

Examiner: Not Yet Assigned

Filed: June 2, 2006

For: CHIP SCALE PACKAGE AND METHOD OF ASSEMBLING THE SAME

**RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS UNDER 35 U.S.C.  
371 IN THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US)**

**MAIL STOP PCT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the "Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US)," for the above application, Applicant respectfully submits herewith a copy of the Declaration for the above identified application properly executed by the inventors.

The statutory fee of \$130.00 is being paid via EFS filing screen. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

  
Carl J. Pellegrini  
Registration No. 40,766

SUGHRUE MION, PLLC  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860

WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

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